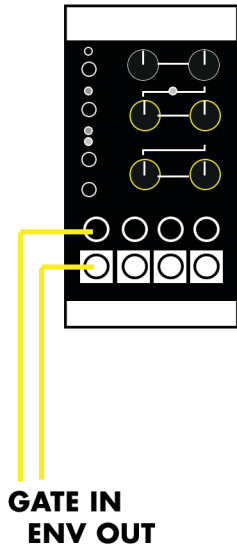
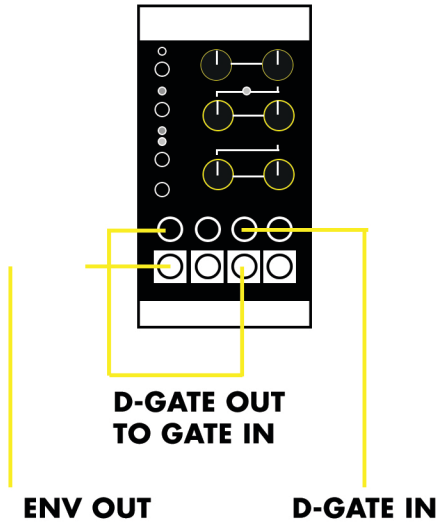


VINTAGE SYNTH LAB D-ENV BASIC PATCH EXAMPLES

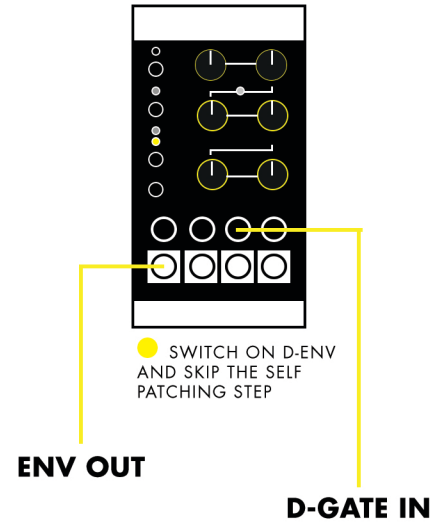
ADSR



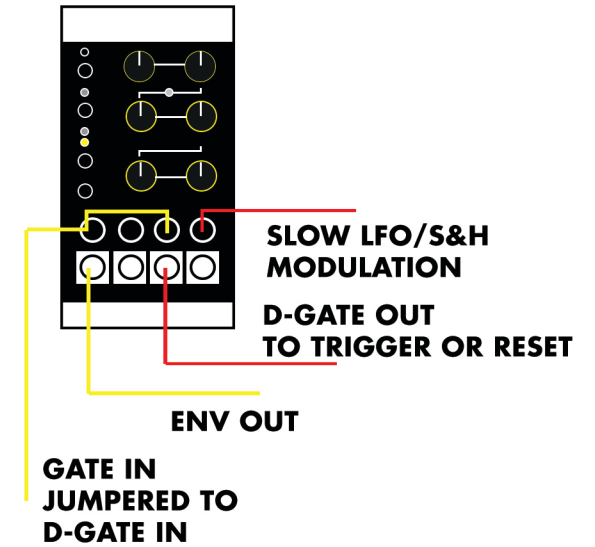
DELAYED ADSR THE HARD WAY



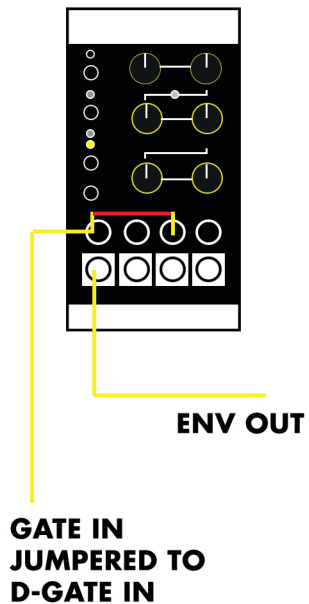
DELAYED ADSR THE EASY WAY



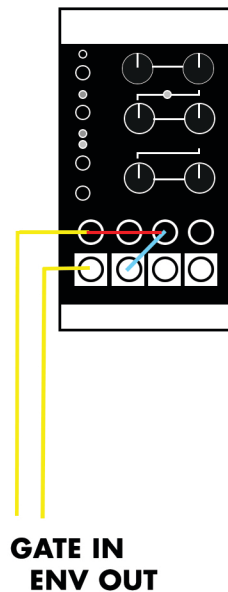
VARIABLE DOUBLE GATE



DOUBLE GATE



CYCLING ADSR



● ENABLE CYCLE

SET DELAY POT TO FULL CLOCKWISE
JUMP CONNECT (STACKABLE CABLE)
GATE INPUT AND D-G INPUT

PATCH A CABLE BETWEEN INV OUT
AND D-G INPUT (MOMENTARILY)

ONCE THE TRIGGER IS RECEIVED
REMOVE THE CABLE FROM INV OUT
TO D-G OUT WITHIN 1 SECOND

* DELAY TIMING MUST ALWAYS
BE GREATER THAN HOLD
TIMING (OR ELSE CYCLE STOPS)

CLOCKED DOUBLE GATE

